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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10021864	FILING DATE 12/17/2001	CLASS 230 363	SUBCLASS 73	GAU 2817 2838	EXAMINER CHOE CHOE
**APPLICANTS: Martin Aaron; Comer David; 2817 230					
BEST AVAILABLE COPY					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED:					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO 884.681US1	
Verified and Acknowledged Examiners's initials					
TITLE : Voltage multiplier circuit					
<small>U.S. DEPT. OF COMM./PAT. & TM.-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
Assistant Examiner		DRAWING	
		Sheet to Draw.	Fig. Draw. Print Fig.
ISSUE FEE			
Amount Due	Date Paid		
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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